

## Zuordnungen für die Platine 9.2.2 vom Strippenstrolch:

### PICAXE-08M2

● = Pin auf dem Picaxe-Board

+V	1	8	0V
(In) Serial In / C.5	2 <b>NC 17</b>	7	C.0 / Serial Out (Out / hserout / DAC)
(Touch / ADC / Out / In) C.4	3 <b>1 16</b>	6	C.1 (In / Out / ADC / Touch / hserin / SRI / hi2c scl)
(In) C.3	4 <b>2 15</b>	5	C.2 (In / Out / ADC / Touch / pwm / tune / SRQ / hi2c sda)

### PICAXE-14M2

● = Pin auf dem Picaxe-Board

+V	1	14	0V
(In) Serial In / C.5	2 <b>NC 17</b>	13	B.0 / Serial Out (Out / hserout / DAC)
(Touch / ADC / Out / In) C.4	3 <b>1 16</b>	12	B.1 (In / Out / ADC / Touch / SRI / hserin)
(In) C.3	4 <b>2 15</b>	11	B.2 (In / Out / ADC / Touch / pwm / SRQ)
(kb clk / hpwm A / pwm / Out / In) C.2	5 <b>3 14</b>	10	B.3 (In / Out / ADC / Touch / hi2c scl)
(kb data / hpwm B / Out / In) C.1	6 <b>4 13</b>	9	B.4 (In / Out / ADC / Touch / pwm / hi2c sda)
(hpwm C / pwm / Touch / ADC / Out / In) C.0	7 <b>5 12</b>	8	B.5 (In / Out / ADC / Touch / hpwm D)

### PICAXE-20M2

● = Pin auf dem Picaxe-Board

+V	1	20	0V
Serial In	2 <b>NC 17</b>	19	Serial Out (DAC)
(Touch / ADC / Out / In) C.7	3 <b>1 16</b>	18	B.0 (In / Out / ADC / Touch / SRI)
(In) C.6	4 <b>2 15</b>	17	B.1 (In / Out / ADC / Touch / SRQ / pwm)
(hpwm A / pwm / Out / In) C.5	5 <b>3 14</b>	16	B.2 (In / Out / ADC / Touch)
(hpwm B / Out / In) C.4	6 <b>4 13</b>	15	B.3 (In / Out / ADC / Touch)
(hpwm C / pwm / Touch / ADC / Out / In) C.3	7 <b>5 12</b>	14	B.4 (In / Out / ADC / Touch / hpwm D)
(kb clk / pwm / Touch / ADC / Out / In) C.2	8 <b>6 11</b>	13	B.5 (In / Out / ADC / Touch / hi2c sda)
(kb data / Touch / ADC / Out / In) C.1	9 <b>7 10</b>	12	B.6 (In / Out / ADC / Touch / hserin)
(hserout / Out / In) C.0	10 <b>8 9</b>	11	B.7 (In / Out / hi2c scl)

### PICAXE-20X2

● = Pin auf dem Picaxe-Board

+V	1	20	0V
Serial In	2 <b>NC 17</b>	19	A.0 / Serial Out (Out)
(ADC3 / Out / In) C.7	3 <b>1 16</b>	18	B.0 (In / Out / ADC1 / hint1)
(In) C.6	4 <b>2 15</b>	17	B.1 (In / Out / ADC2 / hint2 / SRQ)
(hpwm A / pwm C.5 / Out / In) C.5	5 <b>3 14</b>	16	B.2 (In / Out / ADC4 / Comp2+)
(hpwm B / SRNQ / Out / In) C.4	6 <b>4 13</b>	15	B.3 (In / Out / ADC5 / Comp2-)
(hpwm C / ADC7 / Out / In) C.3	7 <b>5 12</b>	14	B.4 (In / Out / ADC6 / hpwm D / Comp1-)
(kb clk / ADC8 / Out / In) C.2	8 <b>6 11</b>	13	B.5 (In / Out / ADC10 / hi2c sda / hspi sdi)
(hspi sdo / kb data / ADC9 / Out / In) C.1	9 <b>7 10</b>	12	B.6 (In / Out / ADC11 / hserin)
(hserout / Out / In) C.0	10 <b>8 9</b>	11	B.7 (In / Out / hi2c scl / hspi sck)